MP-08 (D) DIGITAL ELECTRONICS

(Questions will be set from each unit/section with internal choice)

Units	Pushpontons of the Topics
-1	Number Systems: Binary, Octal and Hexadecimal number systems and their inter conversions. Binary arithmetic, 2's complement, 2's complement arithmetic.
[*] (apior	Boolean Algebra and Logic Circuits: Theorems of Boolean algebra, AND, OR, NAND, NOR, XOR and XNOR gates, minimization techniques, Kamaught map and combinational logic circuits. Arithmetic circuits, Half adder, Full adder, Half subtractor and Full subtractor, Carry look ahead generator, faster method of implementing adder/subtractor, multiplicator.
wi bin erio	Binary Codes: CD, Excess-3, Gray, Binary to Gray and Gray to Binary conversions, Parity check codes, ASCII, EBCDIC and Hollerith codes.
o (H) acristo alteral con our confloor our confloor	Switching behaviour of diode, transistor and other semiconductor devices. Bipolar Logic Families: RTL, DTL, DCTL, TTL, Schottky TTL, HTL, ECL, I ² L. MOS Families: PMOS p-channel MOSFETs, NMOS n-channel MOSFETs and CMOS complementary MOSFETs. TTL to CMOS and vice versa interface. Multivibrators and Waveform Generators: Astable, monostable and bistable Multivibrators, Schmitt trigger, Miller and Bootstrap time base
taniii juolyoj	generators, blocking oscillator, staircase generator.
ts and e⊪e ly Relaxatio schis), Elgki	Registers: Serial and Parallel Shift Registers. Multiplexers and its applications,
s musumus, Heritakanda Herri b atwas	Counters: Binary ripple counter, parallel or synchronous counters, BCD counter, up-down counter, ring counter, shift counter (Johnson counter) & counter applications - frequency counter and digital clock.
IV	Active Filters: low pass, high pass Butterworth filers, Bandpass filters, Bank reject filters and all pass filters switched capacitor filter.
	Oscillators: Principles, oscillator types, frequency stability, phase shift oscillator, Wien Bridge oscillator, Quadrature oscillator, square wave generator, triangular wave generator, swatooth wave generator.
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Violetia or bac and mestablica predicted incligate to average	Introduction to Digital Computer and Microprocessor: Basic building blocks of digital computers, semiconductor and magnetic memories, cache memory, real and virtual memory, memory organisation and memory address map, Input - Output devices, Data, Address and Control buses. Microprocessor Architecture of Intel 8085: ALU, timing and control unit.
on teluşili	status flags. Assembly language programme for data manipulation, arithmetic/logic operation; delay loop.